

No.1507C

LB1408

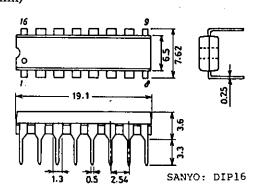
Level Meter

## **Features**

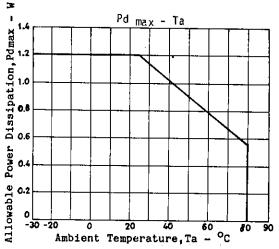
- (1) An input amplifier is built in.
- (2) Minimum number of external parts required.
- (3) Low current dissipation because of series connection of LED's.

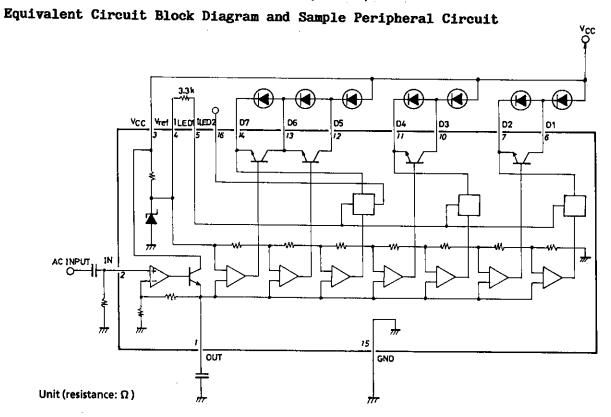
Absolute Maximum Ratings at Ta=25°C									
Maximum Supply Voltage		$v_{cc^{\max}}$	(Pin 3)	-0.	3 to	+18.0	V		
Maximum Input Voltage		V <sub>IN</sub> max				o V <sub>CC</sub>			
D Pin Output Current		$I_{D}^{\mathbf{m}}$ ax	Output transisto	or ON		to 30			
D Pin Output Voltage		$V_{\mathrm{D}}^{\mathrm{D}}$ max				o V <sub>CC</sub>			
Reference Flow-Out Current			(Pin 4)		-3.0 to 0				
Allowable Power Dissipation		Pdmax			_	1.2			
Operating Temperature		Topr		-30 to +80					
Storage Temperature		Tstg			-40 to +125				
Allowable Operating Conditions at Ta=25°C un									
Supply Voltage		$v_{CC}$		6	.7 to	16.0	V		
Electrical Characteristics at Ta=25°C, V <sub>CC</sub> =12V				min	typ	max	unit		
Current Dissipation	$I_{CC}$	Quiescent	pin 3		4	8	mA		
	00	3.3kohms a	across I <sub>LED1</sub>						
		and Vref	Ten I						
Input Bias Current	I <sub>IN</sub>	Pin 2		-10		0	μA		
Reference Voltage	Vref	Pin 4	1	4.40	4.85	5.30	v		
D Pin Current 1	I <sub>D2,4,7</sub>	and Vref	across I <sub>LED</sub> 1	12	16	19	mA		
		ILEDO=GND	pins 7,11,14						
Output Saturation	VsatD	ווייייייייייייייייייייייייייייייייייייי	pins 6,10,12,13		1.0	1.3	V		
Voltage	1,3,5,6						-		
D Pin Current 2	I <sub>D2,4,7</sub>	TI 0	V <sub>CC</sub> =6.7V, 9V,pins 7,11,14	12		19	mA		
OUT Pin Impedance	ROUT	VD1,3,6=0.	9v,pins 7,11,14	8	12	16	kohm		
	501			_					
				Continue	ed on	next	page.		

Package Dimensions 3064 (unit: mm)



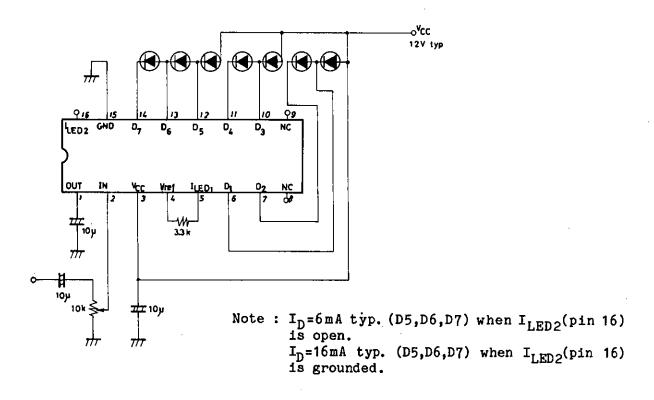
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Input Sensitivity	Y T 1 21	min	typ	max	unit
Tubus Sensitiates	V <sub>IN5</sub> Input voltage at which LED	119	132	145	mV
_	of D5 is lighted				
Comparator Level D1	V <sub>T1</sub> Input voltage at which LED	-26	-20	-14	dΒ
	of D5 is lighted is taken a	as Odb.			
D2	V <sub>T2</sub>	-12	-10	-8	dВ
D3	V <sub>T3</sub> "	-7	-6	_	dΒ
D4	V <sub>T4</sub> "	2 -	-	-	
			-3.0	-2.5	đΒ
D5	V <sub>T5</sub>	0	0	0	dΒ
D <u>6</u>	V <sub>T6</sub>	2.5	3.0	3.5	đΒ
D7	V <sub>T7</sub>	5	6	7	dВ
Output Leakage Current	$I_{DI}^{-1}$ , $V_{TN}=0V$ , pins 6, 10, 12	Ō		10	
<b>5</b>	I <sub>DL</sub> V <sub>IN</sub> =0V,pins 6,10,12 1,3,5	U		10	μA
D Pin Current 3			6 0	0 0	
	I <sub>D7</sub> 3.3kohms across I <sub>LED1</sub> and Vr	ef 4.5	6.0	8.0	mA
D Pin Current 4	I <sub>LED2</sub> =Open, pin 14 V <sub>CC</sub> =6.7V,			_	
D I'm Oullent 4		4.5		8.0	mA
	V <sub>D6</sub> =0.7V,Pin 14				
	<del>-</del>				





## Sample Application Circuit

Unit (resistance:  $\Omega$ , capacitance: F)



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